: IDS filed 9/23/03 (5 pages ATTY. DOCKET NO. FORM PTO-1449 (Modified) SERIAL NO. Unassigned 10/667601 FIS920030186US1 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT: APPLICANT'S INFORMATION DISCLOSURE Dureseti Chidambarrao, et al. STATEMENT 9/23/2003 FILING DATE: GROUP: (Use several sheets if necessary) Concurrently Herewith Unassigned. **U.S. PATENT DOCUMENTS** REFERENCE DESIGNATION FILING DATE **EXAMINER** DOCUMENT NUMBER DATE **CLASS SUBCLASS** INITIALS NAME (IF APPRO.) 6,228,694 B1 5/8/2001 Doyle et al. 6,406,973 B1 6/18/2002 Lee 8/28/2001 6,281,532 B1 Doyle et al. 5,683,934 11/4/97 Candelaria 6,368,931 B1 4/9/2002 Kuhn, et al. 5,310,446 5/10/94 Konishi et al. 4,853,076 8/1/89 Tsaur et al. US 2002/0090791 A1 7/11/2002 Doyle et al. US 2002/0074598 A1 6/20/2002 Doyle et al. 6,509,618 B2 7/21/2003 Jan et al. 6,476,462 B2 11/5/2002 Shimizu et al. 6,362,082 B1 3/26/2002 Doyle et al. 6,228,694 B1 5/8/2001 Doyle et al. 5,565,697 10/15/96 Asakawa et al. US 2003/0040158 A1 2/27/2003 Saitoh US 2002/0086472 A1 7/4/2002 Roberds et al. 6,521,964 B1 2/18/2003 Jan et al. 6,506,652 01/14/03 Jan, et al. FOREIGN PATENT DOCUMENTS DOCUMENT **TRANSLATION** NUMBER DATE COUNTRY SUBCLASS CLASS NO OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.) M. Kennedy Date Considered EXAMINER/ EXAMINER Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if

not in conformance and not considered. Include copy of this form with next communication to applicant.

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a	5,081,513	1/14/1992	Jackson, et al.				
Cu	3,602,841	8/31/1971	McGroddy				
Cal	6,531,740	3/11/2003	Bosco, et al.				
du	6,531,369	3/11/2003	Ozkan, et al.				
bu	6,501,121	12/31/2002	Yu, et al.				
gà l	6,498,358	12/24/2002	Lach, et al.				
al	6,493,497	12/10/2002	Ramdani, et al.				
10	6,403,975	6/11/2002	Brunner, et al.		17		
CAL	6,361,885	3/26/2002	Chou		/		
au	6,255,169	7/3/2001	Li, et al.				
M	6,246,095	6/12/2001	Brady, et al.	/			
CIL	6,165,383	12/26/2000	Chou				
CAR	6,133,071	10/17/2000	Nagai	1			
de	6,046,464	4/4/2000	Schetzina				
U	6,025,280	2/15/2000	Brady, et al.				
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M	5,940,736	8/17/1999	Brady, et al.				
Pul	5,880,040	3/9/1999	Sun, et al.	1			
1/1/	5,861,651	1/19/1999	Brasen, et al.				
MI	5,679,965	10/21/1997	Schetzina	_		/	
MA	5,670,798	9/23/1997	Schetzina	1		/	
Car	5,561,302	10/1/1996	Candelaria	<del></del>	<del>/</del>		
hu	5,471,948	12/5/1995	Burroughes, et al.		<del>  /-</del>		
(1/1)	5,459,346	10/17/1995	Asakawa, et al.		+/-		
P10	5,391,510	2/21/1995	Hsu, et al.		1/		
CRI	5,371,399	12/6/1994	Burroughes, et al.				
Tu	5,108,843	4/28/1992	Ohtaka, et al.		<del>/</del>		
CVII	5,060,030	10/22/1991	Hoke		/		
Tu	4,958,213	9/18/1990	Eklund, et al.	1 /	/		
de	4,665,415	5/12/1987	Esaki, et al.	1 /			
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MI	5,989,978	11/23/1999	Peidous			1	
TIM	6,284,626	9/4/2001	Kim	1			
ou	6,274,444	8/14/2001	Wang				
ou	6,261,964	7/17/2001	Wu, et al.				
m	6,221,735	4/24/2001	Manley, et al.			/	
Me	6,117,722	9/12/2000	Wuu, et al.				
ON	6,107,143	8/22/2000	Park, et al.				
Chr	6,090,684	7/18/2000	Ishitsuka, et al.		1 /		
gel	6,066,545	5/23/2000	Doshi, et al.				
all	6,008,126	12/28/1999	Leedy				
Me	5,946,559	8/31/1999	-Leedy		1		
gu	5,840,593	11/24/1998	Leedy	/			
CAC	5,592,018	1/7/1997	Leedy				
Gr.	5,592,007	1/7/1997	Leedy	1			
CALL	5,571,741	11/5/1996	Leedy	1			
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Oll	5,557,122	9/17/1996	Shrivastava, et al.			(= 12110)	
Del !	5,354,695	10/11/1994	Leedy				
CIL	5,134,085	7/28/1992	Gilgen, et al.				
Occ	5,006,913	4/9/1991	Sugahara, et al.				
CH	4,952,524	8/28/1990	Lee, et al.			<del></del>	
CPL	4,855,245	8/8/1989	Neppl, et al.				
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Qu	Kern Rim, et al., "Transconductance Enhancement in Deep Submicron Strained-Si n-MOSFETs", International Electron Devices Meeting, 26, 8, 1, IEEE, September 1998.						
81L	Kern Rim, et al., "Characteristics and Device Design of Sub-100 nm Strained Si N- and PMOSFETs", 2002 Symposium On VLSI Technology Digest of Technical Papers, IEEE, pp. 98-99						
ne	Gregory Scott, et al., "NMOS Drive Current Reduction Caused by Transistor Layout and Trench Isolation Induced Stress", International Electron Devices Meeting, 34.4.1, IEEE, September 1999						
qu	F. Ootsuka, et al., "A Highly Dense, High-Performance 130nm node CMOS Technology for Large Scale System-on-a-Chip Application", International Electron Devices Meeting, 23.5.1, IEEE, April 2000.						
ge	Shinya Ito, et al., "Mechanical Stress Effect of Etch-Stop Nitride and its Impact on Deep Submicron Transistor Design", International Electron Devices Meeting, 10.7.1, IEEE, April 2000.						
gr	A. Shimizu, et al., "Local Mechanical-Stress Control (LMC): A New Technique for CMOS-Performance Enhancement", International Electron Devices Meeting, IEEE, March 2001.						
gul	K. Ota, et al., "Nov International Elect	vel Locally Stra	nined Channel Technique for eeting, 2.2.1, IEEE, Febru	or high Perfori ary 2002.	mance 55nm C	MOS",	
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